

INFORMATION DISCLOSURE CITATION

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Applicant	Hiroshi KUBOTA et al.		
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U.S. PATENT DOCUMENTS

Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS

	Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No
<u>Gw</u>	10-122178	5/12/98	Japan			Abstract
	6-327924	11/29/94	Japan			Abstract
<u>Gw</u>	9-251981	9/22/97	Japan			Abstract

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

<u>Gw</u>	Itsuko SAKAI et al., "SEMICONDUCTOR PROCESSING APPARATUS AND METHOD FOR MANUFACTURING A SEMICONDUCTOR DEVICE", Serial No. 09/955,083 filed September 19, 2001.
<u>Gw</u>	Masashi SAITO et al., "PROCESSING APPARATUS", Serial No. 09/686,370 filed October 12, 2000.

Examiner	<u>George L. Walton</u>	Date Considered	<u>3/22/04</u>
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